# Algorithms in Superconducting Circuits

#### Axel Dahlberg, Marco Roth

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- **2** Superconducting circuits.

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- **2** Superconducting circuits.
- **3** Control over parameters.

1 Theoretical aspect of Grover's algorithm

2 Circuit Implementation

3 Results

#### 4 Conclusion

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- **3** Task: Find  $x_k$  with the least amount of calls to the function f.
- I To evaluate the function f a gate called the oracle  $O_f$  is implemented. The gate does the following

$$|x\rangle \to (-1)^{f(x)} |x\rangle.$$
 (2)

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The function  $f_0$  outputs a 1 if the input is  $0^{\otimes n}$  and otherwise 0.

а

$$|arphi_{\mathsf{a}}
angle = |0,0
angle$$





$$egin{aligned} ertarphi_{\mathsf{a}} & ert = ert 0, 0 
angle \ & \downarrow \quad \mathsf{H}^{\otimes 2} \ & ertarphi_{\mathsf{b}} & ert = (ert 0, 0 
angle + ert 0, 1 
angle + ert 1, 0 
angle + ert 1, 1 
angle)/2 \end{aligned}$$

One iteration of Grover's algorithm on two qubits.



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 $|arphi_{\mathsf{e}}
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ight
angle - \left|0,1
ight
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ight
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ight
angle )/2$ 



# Circuit Implementation

Superconducting circuit with two qubits used for realising Grover's algorithm.



A four-port device with a coplanar waveguide cavity bus coupling two transmon qubits (Fig. taken from [1]).

#### Implementing the C-Phase gate



Flux dependence of transition frequencies (Fig. taken from [1]).

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## Implementing the C-Phase gate



Flux dependence of transition frequencies (Fig. taken from [1]).

- $\blacksquare$  Cavity-qubit interactions produce a frequency shift between the  $|1,1\rangle$  and the  $|0,2\rangle$  state
- **2** This shift can be exploited to create a C-Phase gate:

$$\frac{1}{2}(|0,0\rangle+|0,1\rangle+|1,0\rangle+|1,1\rangle+0\,|0,2\rangle)$$

$$\begin{array}{c} \displaystyle \frac{1}{2}(|0,0\rangle + |0,1\rangle + |1,0\rangle + |1,1\rangle + 0 \left|0,2\rangle\right) \\ \\ \xrightarrow{\text{flux pulse}} \displaystyle \frac{1}{2}(|0,0\rangle + |0,1\rangle + |1,0\rangle + e^{-i\pi} \left|1,1\rangle + e^{+i\pi}0 \left|0,2\rangle\right) \end{array}$$

$$\begin{array}{c} \displaystyle \frac{1}{2} (|0,0\rangle + |0,1\rangle + |1,0\rangle + |1,1\rangle + 0 \, |0,2\rangle) \\ \\ \xrightarrow{\text{flux pulse}} \displaystyle \frac{1}{2} (|0,0\rangle + |0,1\rangle + |1,0\rangle + e^{-i\pi} \, |1,1\rangle + e^{+i\pi} 0 \, |0,2\rangle) \end{array}$$

Which effectively produces the gate:

$$U = \begin{pmatrix} 1 & 0 & 0 & 0 \\ 0 & 1 & 0 & 0 \\ 0 & 0 & 1 & 0 \\ 0 & 0 & 0 & -1 \end{pmatrix}$$

(3)





$$|arphi_{\mathsf{a}}
angle = |0,0
angle$$



$$\begin{split} |\varphi_{a}\rangle &= |0,0\rangle \\ \downarrow \quad \mathsf{R}_{y}^{\pi/2} \otimes \mathsf{R}_{y}^{\pi/2} \\ |\varphi_{b}\rangle &= \frac{1}{2}(|0,0\rangle + |0,1\rangle + |1,0\rangle + |1,1\rangle) \end{split}$$



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#### The C-Phase gate can be used to create

entanglement (Fig. taken from [1]).

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Element		Grover search oracle*			
		<b>f</b> oo	foi	<b>f</b> 10	f11
$\langle 0,0  ho 0,0 angle$	ldeal	1	0	0	0
	Measured	0.81(1)	0.08(1)	0.07(2)	0.065(7)
$\langle 0,\!1 \big  \rho \big  0,\!1 \rangle$	ldeal	0	1	0	0
	Measured	0.066(7)	0.802(9)	0.05(1)	0.054(8)
$\langle 1,\!0 \left  \! \right. \rho \left  \! 1,\!0 \right\rangle$	ldeal	0	0	1	0
	Measured	0.08(1)	0.05(1)	0.82(2)	0.07(1)
$\langle 1,1  ho 1,1 angle$	ldeal	0	0	0	1
	Measured	0.05(2)	0.07(1)	0.06(1)	0.81(1)

#### Table 1 | Summary of algorithmic performance

Fidelity  $F(\rho, \psi) = \langle \psi | \rho | \psi \rangle$  of final states of Grover's algorithm (Figure taken from [1]).

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- **2** Basic algorithms have been implemented in superconducting circuit systems.
- **3** The present architecture can easily be expanded to several qubits.

1 L. DiCarlo, J. M. Chow, J. M. Gambetta, Lev S. Bishop, B. R. Johnson, D. I. Schuster, J. Majer, A. Blais, L. Frunzio, S. M. Girvin & R. J. Schoelkopf *Demonstration of two-qubit algorithms with a superconducting quantum processor* 

Nature 460, 7252 (2009)

- 2 Clarke, J. & Wilhelm, F.K. Superconducting quantum bits Nature 453, 1031 (2008)
- 3 Schoelkopf, R.J. & Girvin, S.M. Wiring up quantum systems Nature 451, 664 (2008)
- 4 Devoret, M.H. & Martinis, J.M. Implementing Qubits with Superconducting Integrated Circuits Quant. Inf. Proc, 3 163 (2004)

$$U=egin{pmatrix} 1&0&0&0\ 0&e^{i\Phi_{01}}&0&0\ 0&0&e^{i\Phi_{10}}&0\ 0&0&0&e^{i\Phi_{11}} \end{pmatrix}$$

•  $\Phi_{01}$  is adjusted by tuning the rising or falling edge of the pulse •  $\Phi_{10}$  is adjusted by varying the amplitude of a simultaneous  $V_L$  pulse (4)

# Creating Phase Gates



Creating  $U_{01}$